

S/N 10/081,818

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Jerome M. Eldridge et al.	Examiner:	Tu-Tu V. Ho
Serial No.:	10/081,818	Group Art Unit:	2818
Filed:	February 20, 2002	Docket:	1303.045US1
Title:	ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS		

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(c)(2), Applicants hereby authorize the Commissioner to charge the fee of \$180.00 as set forth in 37 C.F.R. §1.17(p), to Deposit Account No. 19-0743. Please charge any additional fees or credit any overpayment to Deposit Account No. 19-0743.

08/28/2006 FMETEK11 00000068 190743 10081818

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## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Serial No :10/081,818

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Dkt: 1303.045US1

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications, and Non-Published Applications identifiable by USPTO Serial Number, are no longer required to be provided to the Office. Notification of this change to this effect was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004 and October 19, 2004. Thus, Applicant has not included copies of any US Patents or US Patent Applications identifiable by serial number that may be cited with this submission. Should the Office require copies to be provided, Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

JEROME M. ELDRIDGE ET AL.


By their Representatives,

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P.O. Box 2938  
Minneapolis, MN 55402  
(612) 349-9587

Date

24 Aug '06

By

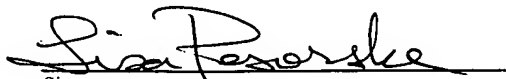
  
Timothy B. Clise  
Reg. No. 40,957

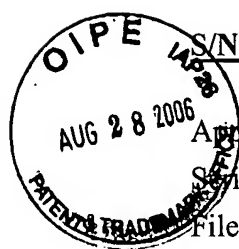
CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this 24 day of August 2006.

Name

Lisa Porsorske

Signature





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Serial No.: 10/081,818

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Title: ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW  
ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS**COMMUNICATION CONCERNING RELATED APPLICATIONS****MS Amendment**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related applications in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date/Issue Date</u>	<u>Attorney Docket</u>	<u>Title</u>
11140643	May 27, 2005	1303.141US1	HAFNIUM TITANIUM OXIDE FILMS
11202460	August 12, 2005	1303.027US3	SERVICE PROGRAMMABLE LOGIC ARRAYS WITH LOW TUNNEL BARRIER INTERPOLY INSULATORS
11212190	August 26, 2005	1303.024US3	INTEGRATED CIRCUIT MEMORY DEVICE AND METHOD
11380599	April 27, 2006	1303.045US3	ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS
11471008	June 20, 2006	1303.035US3	PROGRAMMABLE ARRAY LOGIC OR MEMORY WITH P-CHANNEL DEVICES AND ASYMMETRICAL TUNNEL BARRIERS
11471007	June 20, 2006	1303.035US4	PROGRAMMABLE ARRAY LOGIC OR MEMORY WITH P-CHANNEL DEVICES AND ASYMMETRICAL TUNNEL BARRIERS
11471348	June 20, 2006	1303.035US5	PROGRAMMABLE ARRAY LOGIC OR MEMORY WITH P-CHANNEL DEVICES AND ASYMMETRICAL TUNNEL BARRIERS

## COMMUNICATION CONCERNING RELATED APPLICATIONS

Page 2

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Title: ATOMIC LAYER DEPOSITION OF METAL OXIDE AND/OR LOW ASYMMETRICAL TUNNEL BARRIER INTERPOLY INSULATORS

11492638	July 25, 2006	1303.019US4	DRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS
11492637	July 25, 2006	1303.019US5	DRAM CELLS WITH REPRESSED FLOATING GATE MEMORY, LOW TUNNEL BARRIER INTERPOLY INSULATORS

Continuations and divisionals may be later filed on the cases listed above, or cited to the Examiner in any previous Communication Concerning Related Applications. Applicants request that the Examiner review all continuations and divisionals of the above-listed or previously-cited patent applications before allowing the claims of the present patent application.

Respectfully submitted,

JEROME M. ELDRIDGE ET AL.

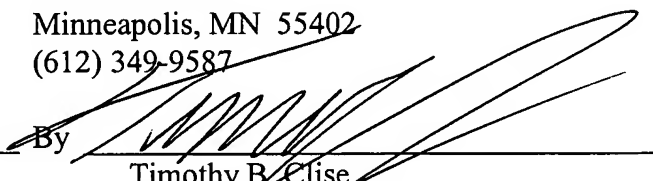
By Applicants' Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
P.O. Box 2938  
Minneapolis, MN 55402  
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Date

24 Aug '06

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Timothy B. Clise  
Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 24 day of August, 2006.

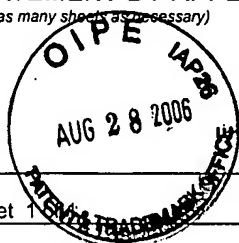
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Name

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Signature

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Sheet 1

Complete if Known

Application Number	10/081,818
Filing Date	February 20, 2002
First Named Inventor	Eldridge, Jerome
Group Art Unit	2818
Examiner Name	Ho, Tu-Tu

Attorney Docket No: 1303.045US1

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
	US-20030042528A1	03/06/2003	Forbes, L.	08/30/2001
	US-20040032773A1	02/19/2004	Forbes, L.	08/08/2003
	US-20040160830A1	08/19/2004	Forbes, L.	02/23/2004
	US-5,798,548	08/25/1998	Fujiwara, H.	05/17/1996
	US-6,091,626	07/18/2000	Madan, S. K.	03/25/1998
	US-6,519,176	02/11/2003	Hamzaoglu, F , et al.	09/29/2000
	US-7,042,043	05/09/2006	Forbes, L. , et al.	08/30/2001
	US-7,068,544	06/27/2006	Forbes, L , et al.	08/30/2001
	US-7,074,673	07/11/2006	Leonard, F.	02/27/2004
	US-7,075,829	07/11/2006	Forbes, L.	08/30/2001
	US-7,087,954	08/08/2006	Forbes, L	08/30/2001

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T <sup>2</sup>
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**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
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**EXAMINER****DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional) <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached